

# PATENT ABSTRACTS OF JAPAN

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(71)Applicant : UCHIHASHI ESTEC CO LTD

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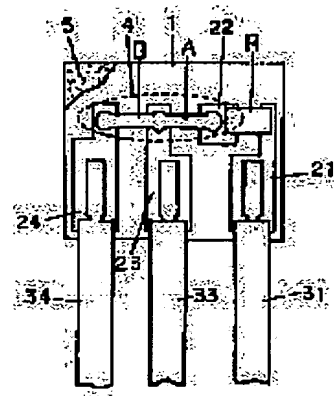
(72)Inventor : UEMURA MITSUAKI

**(54) PROTECTIVE ELEMENT AND ITS APPLICATION METHOD****(57)Abstract:**

**PROBLEM TO BE SOLVED:** To provide a highly functional protective element having a simple structure as well as allowing easy manufacture, regarding a protective element for fusing a low-fusion point fusible alloy piece via the detection of the overvoltage of equipment, and the subsequent feed of power to a film resistor for heat-generation as well as the isolation of the equipment and the film resistor from a power supply.

**SOLUTION:** The first electrode 21, the second electrode 22, the third electrode 23 and the fourth electrode 24 are arranged on one side of an insulating board 1. In addition, a resistor R is provided across the first electrode 21 and the second electrode 22 and low-fusion point fusible alloy pieces A and B are respectively laid between the second electrode 22 and the third electrode 23, and between the third electrode 23 and the fourth electrode 24.

Furthermore, flux 4 is applied to the low-fusion point fusible alloy pieces A and B, and one side of the insulating board 1 is covered with an insulation layer 5.

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**CLAIMS**

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**[Claim(s)]**

[Claim 1] The 1st electrode, the 2nd electrode, the 3rd electrode, and the 4th electrode are prepared on one side of an insulating substrate. Prepare resistance over the 1st electrode and the 2nd electrode, and the piece A of a low-melt point point fusible alloy is connected between the 2nd electrode and the 3rd electrode. The protection component characterized by connecting the piece B of a low-melt point point fusible alloy between the 3rd electrode and the 4th electrode, applying flux to these pieces of a low-melt point point fusible alloy, covering one side of the above-mentioned insulating substrate, covering an insulating layer and changing.

[Claim 2] The 1st electrode, the 2nd electrode, the 3rd electrode, and the 4th electrode are prepared on one side of an insulating substrate. Prepare resistance over the 1st electrode and the 2nd electrode, and the piece A of a low-melt point point fusible alloy is connected between the 2nd electrode and the 3rd electrode. The protection component characterized by connecting the piece B of a low-melt point point fusible alloy between the 2nd electrode and the 4th electrode, applying flux to these pieces of a low-melt point point fusible alloy, covering one side of the above-mentioned insulating substrate, covering an insulating layer and changing.

[Claim 3] The protection component according to claim 1 or 2 which made the inter-electrode die length of the piece B of a low-melt point point fusible alloy longer than the inter-electrode die length of the piece A of a low-melt point point fusible alloy.

[Claim 4] claim 1 which made the melting point of the piece B of a low-melt point point fusible alloy lower than the melting point of the piece A of a low-melt point point fusible alloy thru/or 3 -- the protection component of any or a publication.

[Claim 5] Operation of the protection component characterized by connecting claim 1 thru/or the overvoltage sensing energization circuit which the 3rd electrode of the protection component of a publication is connected to a power-source side, the 4th electrode is connected [ circuit ] to a protected device side 4 either, respectively, and the overvoltage of a protected device is detected [ circuit ] between the 4th electrode and the 1st electrode, and carries out energization generation of heat of the above-mentioned resistance.

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DETAILED DESCRIPTION

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[Detailed Description of the Invention]

[0001]

[Field of the Invention] This invention relates to the protection component used in order to protect an electrical machinery and apparatus from an overvoltage, and its operation.

[0002]

[Description of the Prior Art] When an overvoltage acts on a device and it intercepts a device from a power source, as membrane resistance and the piece of a low-melt point point fusible alloy are installed on one side of an insulating substrate, the protection component which covers one side of an insulating substrate with an insulating layer, and changes is used and it is shown in drawing 5 This protection component E' and overvoltage sensing energization circuit F', for example, circuit F' which connects zener diode D' and grows into the base side of transistor Tr', are inserted between power-source S' and protected device Z'. If the reverse voltage more than the breakdown voltage of zener diode D' acts on device Z', it is well-known to intercept device Z' from power-source S' by base current flowing, collector current flowing according to this base current, membrane resistance R' generating heat, and piece of low-melt point point fusible alloy A' being melted.

[0003] However, even if device z' is intercepted from power-source S' by fusing of piece of low-melt point point fusible alloy A', when device Z' is a KYAPASHITIBU load (for example, when it is a battery), for residual voltage, base current continues flowing, energization generation of heat of membrane resistance R' may be continued, and it is dangerous with above-mentioned protection component E'.

[0004] Then, composite electrode 20' which combined central fuse electrode 21' and single-sided heater T typeface electrode 22' as shown in (b) of drawing 6, As printing formation of other end element T typeface electrode 23' and HI-ZU both-sides electrode 24', and 25' is carried out at substrate 1' and it is shown in (b) of drawing 6 It crosses among T typeface both the arms of single-sided heater T typeface electrode 22' and other end element T typeface electrode 23', respectively Membrane resistance R', As prepare R', insulating-layer i' and i' are prepared on each membrane resistance, piece of low-melt point point fusible alloy A' and A' are connected, respectively between central fuse electrode 21' of composite electrode 20', and fuse both-sides electrode 24' and 25' and it is further shown to (Ha) of drawing 6 Using flux layer 4' and the protection component which covers external insulating-layer 5' and changes is proposed (JP,7-153367,A).

[0005] In the protection network incorporating this protection component, it has 2 sets of pairs of piece of membrane resistance R'-low-melt point point fusible alloy A' as shown in drawing 7, and that piece of a low-melt point point fusible alloy of a pair of is melted by energization generation of heat of the membrane resistance of each set. \*(ing) -- device Z' -- if the reverse voltage more than the breakdown voltage of zener diode D' acts on a side, base current flows to transistor Tr', collector current flows, energization generation of heat of both membrane resistance R' and R' is carried out, and while each piece of low-melt point point fusible alloy A' and A' are melted by energization generation of heat of each membrane resistance and device Z' is intercepted from power-source S', membrane resistance R' and R' will be intercepted from power-source S'.

[0006]

[Problem(s) to be Solved by the Invention] It is necessary to prepare insulating-layer i', for example, glass membrane, in membrane resistance R' up with the above-mentioned protection component. However, with many manufacture man days Since the number of the groups of the piece of a membrane resistance-low-melt point point fusible alloy is two, manufacture is troublesome by the reason of \*\* the resistance adjustment about two membrane resistance is required, and since it is necessary to prepare superficially 2 sets of pieces of a

membrane resistance-low-melt point point fusible alloy, it is disadvantageous also for the formation of small size of a protection component. Furthermore, the insulating layer on membrane resistance, for example, glass membrane, is formed of screen-stencil, and it is hard to avoid it in irregularity-ization resulting from the mesh of a screen, and it cannot guarantee the balling-up fragmentation with the piece of a low-melt point point fusible alloy smooth in this basis on it easily.

[0007] The purpose of this invention detects the overvoltage of a device, carries out energization generation of heat of the membrane resistance, and makes the piece of a low-melt point point fusible alloy melt, for the protection component which intercepts a device and membrane resistance from a power source, it is easy structure and it is to offer the protection component excellent in actuation nature with easy manufacture, and the operation of the protection component.

[0008]

[Means for Solving the Problem] The protection component concerning claim 1 of the invention in this application on one side of an insulating substrate The 1st electrode, Prepare the 2nd electrode, the 3rd electrode, and the 4th electrode, and resistance is prepared over the 1st electrode and the 2nd electrode. It is the configuration which connects the piece A of the NI low-melt point point fusible alloy between the 2nd electrode and the 3rd electrode, connects the piece B of a low-melt point point fusible alloy between the 3rd electrode and the 4th electrode, applies flux to these pieces of a low-melt point point fusible alloy, covers one side of the above-mentioned insulating substrate, covers an insulating layer, and changes. The protection component concerning claim 2 of the invention in this application on one side of an insulating substrate The 1st electrode, Prepare the 2nd electrode, the 3rd electrode, and the 4th electrode, and resistance is prepared over the 1st electrode and the 2nd electrode. It is the configuration characterized by connecting the piece A of a low-melt point point fusible alloy between the 2nd electrode and the 3rd electrode, connecting the piece B of a low-melt point point fusible alloy between the 2nd electrode and the 4th electrode, applying flux to these pieces of a low-melt point point fusible alloy, covering one side of the above-mentioned insulating substrate, covering an insulating layer and changing. the above -- also in which protection component, the melting point of making the inter-electrode die length of the piece B of a low-melt point point fusible alloy longer than the inter-electrode die length of the piece A of a low-melt point point fusible alloy or/and the piece B of a low-melt point point fusible alloy can be made lower than the melting point of the piece A of a low-melt point point fusible alloy. The 3rd electrode is connected to a power-source side, it connects the 4th electrode to a protected device side, respectively, and the protection component concerning these this inventions detects the overvoltage of a protected device between the 4th electrode and the 1st electrode, and is used by connecting the overvoltage sensing energization circuit which carries out energization generation of heat of the above-mentioned resistance.

[0009]

[Embodiment of the Invention] Hereafter, the gestalt of operation of this invention is explained, referring to a drawing. Drawing 1 shows an example of the protection component concerning claim 1. 1 is in drawing 1, heat-resistant insulating substrate, for example, ceramic plate. On one side of an insulating substrate, 21-24 are the film-like electrodes which carried out printing formation, and arrange the 1st electrode 21, the 3rd electrode 23, and the 4th electrode 24 in parallel, and the 2nd electrode 22 is arranged between the point of the 1st electrode 21, and the point of the 3rd electrode 23. The configuration of the 2nd electrode 22 is a square mostly, and the point of the 1st electrode 21 and the point of the 3rd electrode 23 are cut and lacked in order to make spacing with the 2nd electrode 22 into predetermined spacing. Moreover, the point of the 4th electrode 24 is also cut and lacked, in order to fully secure the distance a over the edge of an insulating substrate and to prevent the exsorption from the insulating-substrate edge concerned of the melting low-melt point metal in the time of actuation of a protection component. 31, 33, and 34 are the lead wire (pre-insulation line) linked to each of the 1st electrode 21, the 3rd electrode 23, and the 4th electrode 24. R is the membrane resistance prepared by printing over the point of the 1st electrode 21, and the point of the 2nd electrode 22. It is the piece of a low-melt point point fusible alloy which connected A between the point of the 2nd electrode 22, and the point of the 3rd electrode 23, and the piece of a low-melt point point fusible alloy which connected B between the point of the 3rd electrode 23, and the point of the 4th electrode 24, and when the pieces A and B of a low-melt point point fusible alloy are the same quality of the material and the same configuration, you may make it a successive line. 4 is the flux applied on the pieces A and B of a low-melt point point fusible alloy. 5 has used the insulating

material which can be covered with ordinary temperature, for example, a room-temperature-setting epoxy resin, so that it may be the insulating layer which prepared it as covered one side of an insulating substrate 1 and a melting flow of the above-mentioned piece of a low-melt point point fusible alloy or the flux may not be carried out.

[0010] It is used in order, as for the protection component concerning this invention, to intercept the device from a power source, if an overvoltage acts on a protected device, and drawing 2 is a circuit diagram for explaining the busy condition, and, as for F, the overvoltage protection component actuation circuit is shown for the protection component which E requires for this invention, respectively. In drawing 2, the protection component E and the overvoltage protection component actuation circuit F which start this invention between the protected device Z and a power source S are incorporated. The collector of Transistor Tr is connected to the 1st electrode 21 of the protection component E. The high-voltage lateral electrode of zener diode D and the 4th electrode 24 of the protection component E are connected to the high-voltage side edge child of the protected device Z, the 3rd electrode 23 of the protection component E is connected to the high-voltage side edge child of a power source S, and the emitter of Transistor Tr is grounded. In the circuit shown in drawing 2, if the overvoltage more than the breakdown voltage of zener diode D acts on Device Z Base current flows to Transistor Tr, the collector current which becomes size in connection with this flows, and membrane resistance R generates heat. It is melted this generating heat being transmitted to the pieces A and B of a low-melt point point fusible alloy through the 2nd electrode 22, and the pieces A and B of both the low-melt point point fusible alloy receiving an activity operation of the flux of existing melting, and while the protected device Z is intercepted from a power source S, membrane resistance R is intercepted from a power source. Therefore, after the piece B of a low-melt point point fusible alloy is melted, even if the overvoltage condition of Device Z is maintained for residual charge and Transistor Tr is in switch-on, exoergic continuation of membrane resistance R can be eliminated for the cutoff from the power source S of the membrane resistance R by fusing of the piece A of a low-melt point point fusible alloy.

[0011] Drawing 3 shows an example of the protection component concerning claim 2. In this protection component, the 1st electrode 21, the 3rd electrode 23, and the 4th electrode 24 are formed in parallel on one side of an insulating substrate 1. Separate predetermined spacing, form the 2nd electrode 22, and membrane resistance R is formed in the tip side of these electrodes over the 1st electrode 21 and the 2nd electrode 22. The piece A of a low-melt point point fusible alloy is connected between the 2nd electrode 22 and the 3rd electrode 23, the piece B of a low-melt point point fusible alloy is connected between the 2nd electrode 22 and the 4th electrode 24, flux 4 is applied to the pieces A and B of a low-melt point point fusible alloy, one side of the above-mentioned insulating substrate is covered, and the insulating layer (not shown) is covered.

[0012] Also in use of this protection component, as shown in drawing 4, the collector of Transistor Tr is connected to the 1st electrode 21 of the protection component E, the high-voltage lateral electrode of zener diode D and the 4th electrode 24 of the protection component E are connected to the high-voltage side edge child of the protected device Z, the 3rd electrode 23 of the protection component E is connected to the high-voltage side edge child of a power source S, and the emitter of Transistor Tr is grounded. If it \*\* and the overvoltage more than the breakdown voltage of zener diode D acts on Device Z Base current flows to Transistor Tr, the collector current which becomes size in connection with this flows, and membrane resistance R generates heat. This generating heat is transmitted to the pieces A and B of a low-melt point point fusible alloy through the 2nd electrode 22, the pieces A and B of both the low-melt point point fusible alloy are melted, and while the protected device Z is intercepted from a power source S, membrane resistance R is intercepted from a power source S.

[0013] In the above, it contributes effectively to fusing of a melting low-melt point point metal that an insulating substrate crawls molten metal and that an electrode is well damp in molten metal, and the surface smooth nature of an insulating substrate is also important conditions (element which make it easy to flow molten metal). The ceramic plate is excellent in surface smooth nature compared with the screen-stencil film of glass, and advantageous for \*\*\*\*.

[0014] the above -- also in the protection component concerning which this invention, since the piece B of a low-melt point point fusible alloy is far apart from Resistance R compared with the piece A of a low-melt point point fusible alloy, it is hard to melt. Therefore, it is effective to make the melting point of the piece B of a low-melt point point fusible alloy lower than the melting point of the piece A of a low-melt point point fusible alloy

or to make the inter-electrode die length of the piece B of a low-melt point point fusible alloy longer than the inter-electrode die length of the piece A of a low-melt point point fusible alloy, and to strengthen crawling which made size the touch area to the insulating substrate of the piece B of a low-melt point point fusible alloy, and described it above from the touch area to the insulating substrate of the piece A of a low-melt point point fusible alloy.

[0015] In the protection component concerning this invention, a ceramic plate with a thickness of 100-1200 micrometers, for example, 96% alumina-ceramics plate, can be used for an insulating substrate. In addition, a metal is used as a parent, and use is also possible although insulating processing was carried out. Usually (3mm - 20mm) let the flat-surface dimension of an insulating substrate be the square or rectangle of x (3mm - 20mm). the protection component concerning this invention -- it is and the low-melting-alloys wire whose liquidus-line temperature is 75 degrees C - 300 degree-C diameter of 100 micrometers - 1200 micrometers, the low-melting alloy square wire of the same cross section as this, or a low-melting-alloys foil can be used for the piece of a low-melt point point fusible alloy. the protection component concerning this invention -- it is, and an electrode screen-stencils conductive paste (conductor being the mixture of powder and a cover coat a conductor powder a silver-platinum system, a silver-palladium system, a copper system), and can form it by the ability burning this. Moreover, an insulating substrate with an electrode can also be obtained by etching of the copper foil of a copper foil laminated circuit board.

[0016] the protection component concerning this invention -- it is, membrane resistance screen-stencils the mixture of resistive paste, for example, ruthenium oxide powder, or carbon powder, and a cover coat on an insulating substrate, it can form by the ability burning this, and thickness is usually set to 1-30 micrometers. The membrane resistance of a Ti-Si system can also be used for membrane resistance. The lap condition of a membrane resistance edge and an electrode edge has good any also as an inferior-surface-of-tongue side or a top-face side. It replaces with such membrane resistance and use of a chip resistor is also possible. In the protection component concerning this invention, flux prevents oxidation of the piece of a low-melt point point fusible alloy, and it is used in order to dissolve some oxide films of the piece of a low-melt point point fusible alloy and to make fragmentation of a melting alloy easy, it usually uses rosin as a principal component, and can use what added the activator (for example, hydrochloride of diethylamine) if needed.

[0017] In order to manufacture the protection component concerning this invention, the 1st electrode - the 4th electrode are formed in one side of an insulating substrate. Membrane resistance is printed, trimming adjusts resistance if needed, and the need is accepted. On membrane resistance A protective coat, For example, a glass protective coat is formed, the pieces A and B of a low-melt point point fusible alloy are connected, lead wire is connected to an electrode, flux is applied to the piece of a low-melt point point fusible alloy, subsequently to the epoxy resin liquid of ordinary temperature a substrate is immersed, and the approach of carrying out desiccation hardening of the dipcoat layer can be used.

[0018]

[Effect of the Invention] In the protection component concerning this invention, it has the resistor of a piece, and two pieces of a low-melt point point fusible alloy. It is the configuration which intercepts a resistor from a power source while making both pieces of a low-melt point point fusible alloy melt by energization generation of heat of a resistor and intercepting a protected device from a power source, when an overvoltage acts on a protected device. Compared with the configuration which prepares a resistor to each of the conventional example, i.e., the piece of both the low-melt point point fusible alloy, melts one piece of a low-melt point point fusible alloy by energization generation of heat of one resistor, and melts the piece of a low-melt point point fusible alloy of another side by energization generation of heat of the resistor of another side, it is structurally simple.

[0019] Moreover, prepare insulating glass membrane on membrane resistance, and it differs from the conventional example which has allotted the piece of a low-melt point point fusible alloy in piles on this insulating glass membrane. It allots without piling up membrane resistance and the piece of a low-melt point point fusible alloy, and is the front face (on screen-stencil) of the above-mentioned insulating glass membrane. Since the irregularity resulting from a screen mesh was avoided, and twisted and the twist has also allotted the piece of a low-melt point point fusible alloy on the ceramic insulating substrate on the front face of smooth, a melting alloy is made to flow smoothly, it makes divide quickly and obtains, and the outstanding actuation nature can be guaranteed. Furthermore, when formation of the insulating glass membrane to a membrane

resistance top can be omitted and you need resistance adjustment, since what is necessary is just to perform resistance adjustment by trimming about the membrane resistance of a piece, a manufacture man day can be reduced and it is advantageous on manufacture. Furthermore, good actuation nature can be guaranteed again by the longitudinal adjustment or melting point adjustment between [ two ] the pieces of a low-melt point point fusible alloy, and it is safe.

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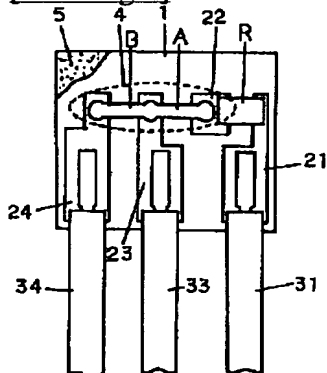
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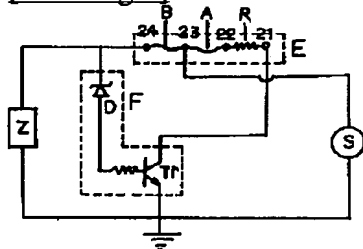
DRAWINGS

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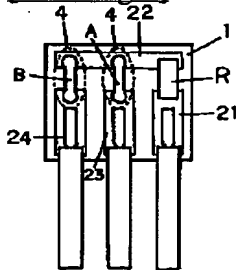
[Drawing 1]



[Drawing 2]

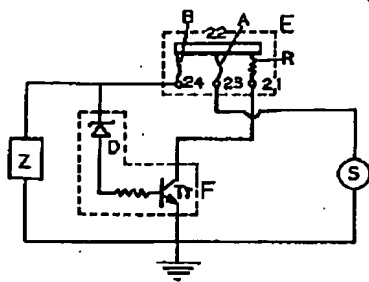


[Drawing 3]

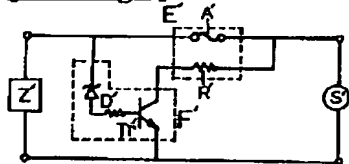


[Drawing 4]

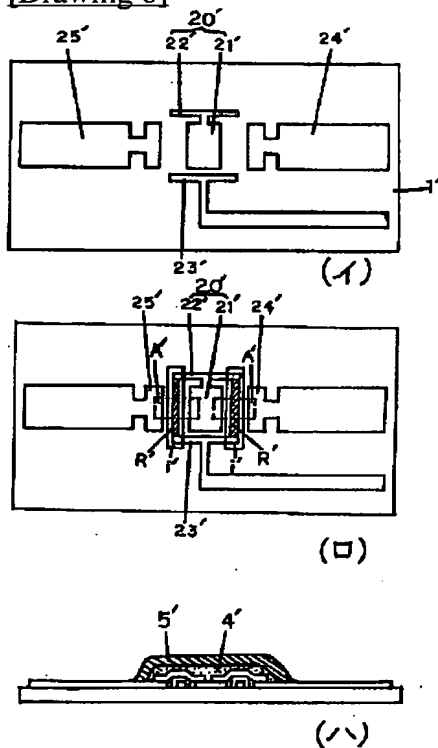




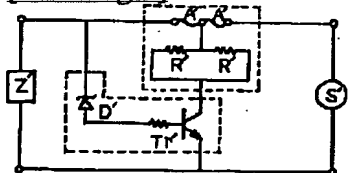
[Drawing 5]



[Drawing 6]



[Drawing 7]



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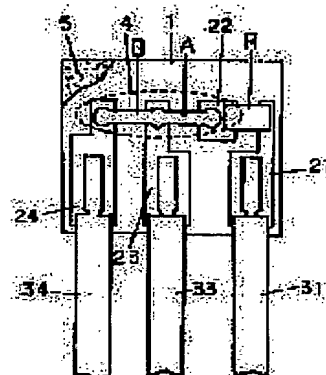
(72)Inventor : UEMURA MITSUAKI

## (54) PROTECTIVE ELEMENT AND ITS APPLICATION METHOD

## (57)Abstract:

**PROBLEM TO BE SOLVED:** To provide a highly functional protective element having a simple structure as well as allowing easy manufacture, regarding a protective element for fusing a low-fusion point fusible alloy piece via the detection of the overvoltage of equipment, and the subsequent feed of power to a film resistor for heat-generation as well as the isolation of the equipment and the film resistor from a power supply.

**SOLUTION:** The first electrode 21, the second electrode 22, the third electrode 23 and the fourth electrode 24 are arranged on one side of an insulating board 1. In addition, a resistor R is provided across the first electrode 21 and the second electrode 22 and low-fusion point fusible alloy pieces A and B are respectively laid between the second electrode 22 and the third electrode 23, and between the third electrode 23 and the fourth electrode 24. Furthermore, flux 4 is applied to the low-fusion point fusible alloy pieces A and B, and one side of the insulating board 1 is covered with an insulation layer 5.



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(71)出願人 000225337

内橋エステック株式会社

大阪府大阪市中央区島之内1丁目11番28号

(72)発明者 植村 充明

大阪市中央区島之内1丁目11番28号 内橋

エステック株式会社内

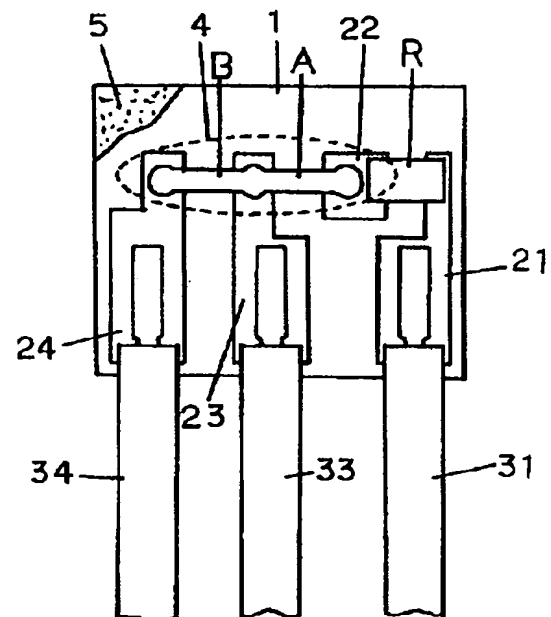
(74)代理人 弁理士 松月 美勝

(54)【発明の名称】 保護素子及びその使用方法

(57)【要約】

【課題】機器の過電圧を検知し膜抵抗を通电発熱させて低熔点可溶合金片を溶断させ、機器及び膜抵抗を電源より遮断する保護素子を対象とし、構造が簡単で製造が容易な作動性に優れた保護素子を提供する。

【解決手段】絶縁基板1の片面上に第1電極21、第2電極22、第3電極23及び第4電極24を設け、第1電極21と第2電極22とにわたって抵抗Rを設け、第2電極22と第3電極23との間及び第3電極23と第4電極24との間に低熔点可溶合金片A及びBをそれぞれ接続し、低熔点可溶合金片にフラックス4を塗布し、上記絶縁基板1の片面を覆って絶縁層5を被覆して成る。



### 【特許請求の範囲】

【請求項 1】絶縁基板の片面上に第 1 電極、第 2 電極、第 3 電極及び第 4 電極を設け、第 1 電極と第 2 電極とにわたって抵抗を設け、第 2 電極と第 3 電極との間に低融点可溶合金片 A を接続し、第 3 電極と第 4 電極との間に低融点可溶合金片 B を接続し、これらの低融点可溶合金片にフラックスを塗布し、上記絶縁基板の片面を覆って絶縁層を被覆して成ることを特徴とする保護素子。

【請求項 2】絶縁基板の片面上に第 1 電極、第 2 電極、第 3 電極及び第 4 電極を設け、第 1 電極と第 2 電極とにわたって抵抗を設け、第 2 電極と第 3 電極との間に低融点可溶合金片 A を接続し、第 2 電極と第 4 電極との間に低融点可溶合金片 B を接続し、これらの低融点可溶合金片にフラックスを塗布し、上記絶縁基板の片面を覆って絶縁層を被覆して成ることを特徴とする保護素子。

【請求項 3】低融点可溶合金片 B の電極間長さを低融点可溶合金片 A の電極間長さよりも長くした請求項 1 または 2 記載の保護素子。

【請求項 4】低融点可溶合金片 B の融点を低融点可溶合金片 A の融点より低くした請求項 1 乃至 3 何れか記載の保護素子。

【請求項 5】請求項 1 乃至 4 何れか記載の保護素子の第 3 電極を電源側に、第 4 電極を被保護機器側にそれぞれ接続し、第 4 電極と第 1 電極との間に被保護機器の過電圧を検知し、上記抵抗を通電発熱させる過電圧検知通電回路を接続することを特徴とする保護素子の使用方法。

### 【発明の詳細な説明】

#### 【0001】

【発明の属する技術分野】本発明は過電圧から電気機器を保護するために用いる保護素子及びその使用方法に関するものである。

#### 【0002】

【従来の技術】機器に過電圧が作用したときに機器を電源から遮断する場合、膜抵抗と低融点可溶合金片とを絶縁基板の片面上に並設し、絶縁基板の片面を絶縁層で被覆して成る保護素子を使用し、図 5 に示すように、この保護素子 E' と過電圧検知通電回路 F'、例えば、トランジスタ Tr' のベース側にツエナダイオード D' を接続して成る回路 F' とを電源 S' と被保護機器 Z' との間に挿入し、機器 Z' にツエナダイオード D' の降伏電圧以上の逆電圧が作用すると、ベース電流が流れ、このベース電流に応じコレクタ電流が流れて膜抵抗 R' が発熱し、低融点可溶合金片 A' が溶断されることで機器 Z' を電源 S' から遮断することが公知である。

【0003】しかしながら、上記の保護素子 E' では、低融点可溶合金片 A' の溶断で機器 Z' が電源 S' から遮断されても、機器 Z' がキャパシティブな負荷の場合、例えば蓄電池の場合、残留電圧のためにベース電流が流れ続けて膜抵抗 R' の通電発熱が継続されることがあり、危険である。

【0004】そこで、図 6 の (イ) に示すように、中央ヒューズ電極 21' と片側ヒーター T 字形電極 22' とを結合した複合電極 20'、他側ヒーター T 字形電極 23' 及びヒーズ両側電極 24'、25' とを基板 1' に印刷形成し、図 6 の (ロ) に示すように、片側ヒーター T 字形電極 22' と他側ヒーター T 字形電極 23' との T 字形両アーム間にわたってそれぞれ膜抵抗 R'、R' を設け、各膜抵抗上に絶縁層 i'、i' を設け、複合電極 20' の中央ヒューズ電極 21' とヒューズ両側電極 24'、25' との間にそれぞれ低融点可溶合金片 A'、A' を接続し、更に図 6 の (ハ) に示すように、フラックス層 4'、外部絶縁層 5' を被覆して成る保護素子を用いることが提案されている（特開平 7-153367 号）。

【0005】この保護素子を組み込んだ保護回路では、図 7 に示す通り膜抵抗 R' - 低融点可溶合金片 A' の対を 2 組備え、各対の膜抵抗の通電発熱でその対の低融点可溶合金片を溶断している。而して、機器 Z' 側にツエナダイオード D' の降伏電圧以上の逆電圧が作用すると、トランジスタ Tr' にベース電流が流れ、コレクタ電流が流れて両膜抵抗 R'、R' が通電発熱され、各膜抵抗の通電発熱で各低融点可溶合金片 A'、A' が溶断されて機器 Z' が電源 S' から遮断されると共に膜抵抗 R'、R' が電源 S' から遮断される。

#### 【0006】

【発明が解決しようとする課題】しかしながら、上記の保護素子では膜抵抗 R' 上に絶縁層 i'、例えばガラス膜を設ける必要があり製造工数が多い、膜抵抗 - 低融点可溶合金片の組が二対であるために 2 個の膜抵抗についての抵抗値調整が必要である、等の理由により製造がやっかいであり、また、膜抵抗 - 低融点可溶合金片を平面的に 2 組設ける必要があるため保護素子の小サイズ化にも不利である。更に、膜抵抗上の絶縁層、例えばガラス膜はスクリーン印刷により形成され、スクリーンのメッシュに起因しての凹凸化が避け難く、かかるものでは、その上での低融点可溶合金片の円滑な球状化分断を保証し難い。

【0007】本発明の目的は、機器の過電圧を検知し膜抵抗を通電発熱させて低融点可溶合金片を溶断させ、機器及び膜抵抗を電源より遮断する保護素子を対象とし、構造が簡単で製造が容易な作動性に優れた保護素子及びその保護素子の使用方法を提供することにある。

#### 【0008】

【課題を解決しようとする手段】本願発明の請求項 1 に係る保護素子は、絶縁基板の片面上に第 1 電極、第 2 電極、第 3 電極及び第 4 電極を設け、第 1 電極と第 2 電極とにわたって抵抗を設け、第 2 電極と第 3 電極との間に低融点可溶合金片 A を接続し、第 3 電極と第 4 電極との間に低融点可溶合金片 B を接続し、これらの低融点可溶合金片にフラックスを塗布し、上記絶縁基板の片面を覆

って絶縁層を被覆して成る構成である。本願発明の請求項2に係る保護素子は、絶縁基板の片面上に第1電極、第2電極、第3電極及び第4電極を設け、第1電極と第2電極とにわたって抵抗を設け、第2電極と第3電極との間に低融点可溶合金片Aを接続し、第2電極と第4電極との間に低融点可溶合金片Bを接続し、これらの低融点可溶合金片にフラックスを塗布し、上記絶縁基板の片面を覆って絶縁層を被覆して成ることを特徴とする構成である。上記何れの保護素子においても、低融点可溶合金片Bの電極間長さを低融点可溶合金片Aの電極間長さよりも長くすること、または、及び低融点可溶合金片Bの融点を低融点可溶合金片Aの融点より低くすることができる。これら本発明に係る保護素子は、第3電極を電源側に、第4電極を被保護機器側にそれぞれ接続し、第4電極と第1電極との間に被保護機器の過電圧を検知し、上記抵抗を通電発熱させる過電圧検知通電回路を接続することにより使用される。

#### 【0009】

【発明の実施の形態】以下、図面を参照しつつ本発明の実施の形態について説明する。図1は請求項1に係る保護素子の一例を示している。図1において、1は耐熱性の絶縁基板、例えばセラミックス板である。21~24は絶縁基板の片面上に印刷形成した膜状電極であり、第1電極21と第3電極23と第4電極24とを並行に配設し、第2電極22は第1電極21の先端部と第3電極23の先端部との間に配設してある。第2電極22の形状はほぼ正方形であり、第1電極21の先端部及び第3電極23の先端部は第2電極22との間隔を所定の間隔とするために切り欠いてある。また、第4電極24の先端部も、絶縁基板の端縁に対する距離aを充分に確保して保護素子の作動時での熔融低融金属の当該絶縁基板端縁からの漏出を防止するために切り欠いてある。31、33及び34は第1電極21、第3電極23及び第4電極24のそれぞれに接続したリード線（絶縁被覆線）である。Rは第1電極21の先端部と第2電極22の先端部とにわたって印刷により設けた膜抵抗である。Aは第2電極22の先端部と第3電極23の先端部との間に接続した低融点可溶合金片、Bは第3電極23の先端部と第4電極24の先端部との間に接続した低融点可溶合金片であり、低融点可溶合金片A、Bが同一材質、同一形状の場合、連続線にしてもよい。4は低融点可溶合金片A及びB上に塗布したフラックスである。5は絶縁基板1の片面を覆うようにして設けた絶縁層であり、上記低融点可溶合金片やフラックスを熔融流動させることなく、常温で被覆できる絶縁材、例えば常温硬化エポキシ樹脂を使用してある。

【0010】本発明に係る保護素子は、被保護機器に過電圧が作用すると、その機器を電源から遮断するために使用され、図2はその使用状態を説明するための回路図であり、Eは本発明に係る保護素子を、Fは過電圧保護

素子作動回路をそれぞれ示している。図2において、被保護機器Zと電源Sとの間に本発明に係る保護素子Eと過電圧保護素子作動回路Fを組み込み、トランジスタTrのコレクタを保護素子Eの第1電極21に接続し、ツエナダイオードDの高電圧側電極及び保護素子Eの第4電極24を被保護機器Zの高電圧側端子に接続し、保護素子Eの第3電極23を電源Sの高電圧側端子に接続し、トランジスタTrのエミッタを接地してある。図2に示す回路において、機器ZにツエナダイオードDの降伏電圧以上の過電圧が作用すると、トランジスタTrにベース電流が流れ、これに伴い大なるコレクタ電流が流れて膜抵抗Rが発熱され、この発生熱が第2電極22を介し低融点可溶合金片A及びBに伝達されて両低融点可溶合金片A及びBが既熔融のフラックスの活性作用を受けつつ溶断され、被保護機器Zが電源Sから遮断されると共に膜抵抗Rが電源から遮断される。従って、低融点可溶合金片Bが溶断されたのち、機器Zの過電圧状態が残留電荷のために維持されてトランジスタTrが導通状態にあっても、低融点可溶合金片Aの溶断による膜抵抗Rの電源Sからの遮断のために、膜抵抗Rの発熱続行を排除できる。

【0011】図3は請求項2に係る保護素子の一例を示している。この保護素子においては、絶縁基板1の片面上に第1電極21、第3電極23及び第4電極24を並行に設け、これら電極の先端側に所定の間隔を隔てて第2電極22を設け、第1電極21と第2電極22とにわたって膜抵抗Rを設け、第2電極22と第3電極23との間に低融点可溶合金片Aを接続し、第2電極22と第4電極24との間に低融点可溶合金片Bを接続し、低融点可溶合金片A、Bにフラックス4を塗布し、上記絶縁基板の片面を覆って絶縁層（図示せず）を被覆してある。

【0012】この保護素子の使用においても、図4に示すように、トランジスタTrのコレクタを保護素子Eの第1電極21に接続し、ツエナダイオードDの高電圧側電極及び保護素子Eの第4電極24を被保護機器Zの高電圧側端子に接続し、保護素子Eの第3電極23を電源Sの高電圧側端子に接続し、トランジスタTrのエミッタを接地する。而して、機器ZにツエナダイオードDの降伏電圧以上の過電圧が作用すると、トランジスタTrにベース電流が流れ、これに伴い大なるコレクタ電流が流れて膜抵抗Rが発熱され、この発生熱が第2電極22を介し低融点可溶合金片A及びBに伝達されて両低融点可溶合金片A及びBが溶断され、被保護機器Zが電源Sから遮断されると共に膜抵抗Rが電源Sから遮断される。

【0013】上記において、熔融低融点金属の溶断には、絶縁基板が熔融金属をはじくことと、電極が熔融金属によく濡れることが有効に寄与し、絶縁基板の表面平滑性も重要な条件である（熔融金属を流れ易くする要

素)。而るに、セラミックス板は、ガラスのスクリーン印刷膜に較べ表面平滑性に優れており有利である。

【0014】上記何れの本発明に係る保護素子においても、低熔点可溶合金片Bは低熔点可溶合金片Aに較べ抵抗Rから隔たっているため、溶断し難い。従って、低熔点可溶合金片Bの融点を低熔点可溶合金片Aの融点より低くすること、または低熔点可溶合金片Bの電極間長さを低熔点可溶合金片Aの電極間長さよりも長くして低熔点可溶合金片Bの絶縁基板に対する接触面積を低熔点可溶合金片Aの絶縁基板に対する接触面積より大にし上記したはじきを強くすることが有効である。

【0015】本発明に係る保護素子において、絶縁基板には厚み100～1200 $\mu$ mのセラミックス板、例えば、96%アルミナセラミックス板を使用できる。その他、金属を母体とし、絶縁処理したものの使用も可能である。絶縁基板の平面寸法は、通常(3mm～20mm)×(3mm～20mm)の正方形乃至は長方形とされる。本発明に係る保護素子において、低熔点可溶合金片には液相線温度が75℃～300℃直径100 $\mu$ m～1200 $\mu$ mの低熔点合金丸線、これと同一断面積の低熔点合金角線または低熔点合金箔を使用できる。本発明に係る保護素子において、電極は導体ペースト(導体粉末と釉薬との混合物であり、導体粉末には銀-白金系、銀-パラジウム系、銅系)をスクリーン印刷し、これを焼き付けることにより形成できる。また、銅箔積層基板の銅箔のエッチングにより電極付き絶縁基板を得ることもできる。

【0016】本発明に係る保護素子において、膜抵抗は抵抗ペースト、例えば、酸化ルテニウム粉末または炭素粉末と釉薬との混合物を絶縁基板上にスクリーン印刷し、これを焼き付けることにより形成でき、膜厚は通常1～30 $\mu$ mとされる。膜抵抗にはTi-Si系の膜抵抗を使用することもできる。膜抵抗端部と電極端部との重なり状態は、何れが下面側または上面側としてもよい。これらの膜抵抗に代え、チップ抵抗の使用も可能である。本発明に係る保護素子において、フラックスは低熔点可溶合金片の酸化を防止し、かつ低熔点可溶合金片の多少の酸化膜を溶解して溶融合金の分断を容易にするために用いられ、通常ロジン系を主成分とし、必要に応じて活性剤(例えば、ジエチルアミンの塩酸塩)を添加したものを使用できる。

【0017】本発明に係る保護素子を製造するには、絶縁基板の片面に第1電極～第4電極を形成し、膜抵抗を印刷し、必要に応じてトリミングにより抵抗値を調整し、必要に応じて膜抵抗上に保護膜、例えばガラス保護膜を形成し、低熔点可溶合金片A及びBを接続し、電極にリード線を接続し、低熔点可溶合金片にフラックスを塗布し、次いで基板を常温のエポキシ樹脂液に浸漬し、その浸漬被覆層を乾燥硬化させる方法を使用できる。

【0018】

【発明の効果】本発明に係る保護素子においては、一個の抵抗と二個の低熔点可溶合金片を有し、被保護機器に過電圧が作用したときに抵抗の通電発熱により両方の低熔点可溶合金片を溶断させて被保護機器を電源から遮断すると共に抵抗を電源から遮断する構成であり、従来例、すなわち、両低熔点可溶合金片のそれぞれに対し抵抗を設け、一方の抵抗の通電発熱で一方の低熔点可溶合金片を溶断し、他方の抵抗の通電発熱で他方の低熔点可溶合金片を溶断する構成に較べ、構造的に簡易である。

【0019】また、膜抵抗上に絶縁ガラス膜を設け、この絶縁ガラス膜上に低熔点可溶合金片を重ねて配している従来例とは異なり、膜抵抗と低熔点可溶合金片とを重ねずに配し、上記絶縁ガラス膜の表面(スクリーン印刷上、スクリーンメッシュに起因する凹凸が避けられない)よりも平滑表面のセラミックス絶縁基板上に低熔点可溶合金片を配しているから、溶融合金をスムーズに流動させて迅速に分断させ得、優れた作動性を保証できる。更に、膜抵抗上への絶縁ガラス膜の形成を省略でき、また、抵抗値調整を必要とする場合は、トリミングによる抵抗値調整を一個の膜抵抗について行えばよいから、製造工数を低減でき、製造上有利である。更にまた、二個の低熔点可溶合金片相互間での長さ調整、または融点調整で良好な作動性を保証でき、安全である。

【図面の簡単な説明】

【図1】請求項1に係る保護素子を示す説明図である。

【図2】請求項1に係る保護素子を用いた保護回路を示す説明図である。

【図3】請求項2に係る保護素子を示す説明図である。

【図4】請求項2に係る保護素子を用いた保護回路を示す説明図である。

【図5】従来の保護回路を示す説明図である。

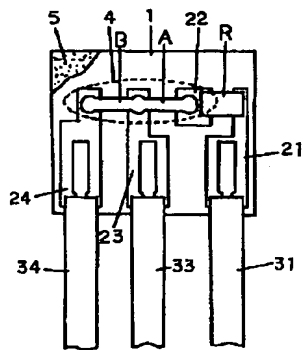
【図6】従来の保護素子を示す説明図である。

【図7】図6の保護素子を用いた保護回路を示す説明図である。

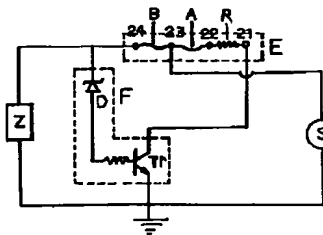
【符号の説明】

1	絶縁基板
2 1	第1電極
2 2	第2電極
2 3	第3電極
2 4	第4電極
A	低熔点可溶合金片
B	低熔点可溶合金片
R	抵抗
4	フラックス
5	絶縁層
E	保護素子
F	過電圧検知通電回路
Z	被保護機器
S	電源

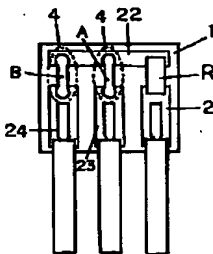
【圖 1】



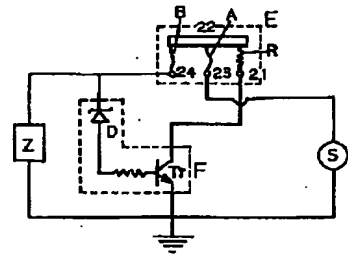
【圖 2】



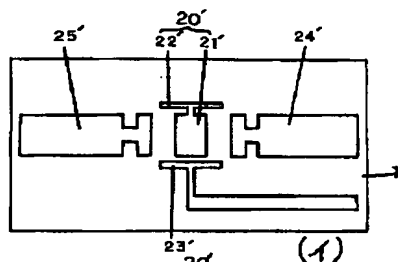
【圖 3】



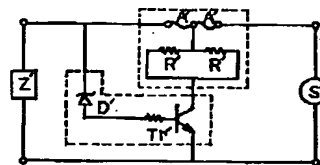
【圖 4】



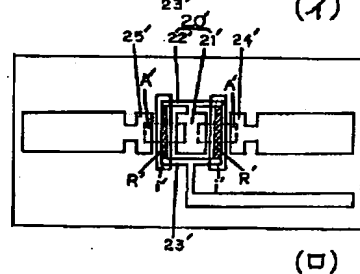
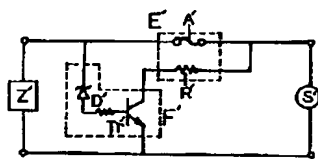
【圖 6】



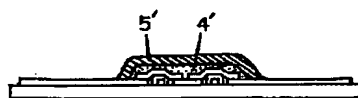
【圖 7】



【圖 5】



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